

## 2T XOR-Based Content Addressable Memory

In this project, a novel 2T XOR module is introduced to replace 4T XOR module in order to achieve high speed, low power consumption, low area in Parameter extractor. Parameter extractor is the main part of the CAM that consumes more power and it uses an array of XOR gates which leads to more power dissipation. Simulation results show that the 2T XOR module consumes less power and high speed when compared with 14T and 4T XOR gates.

**Domain:** Back End Domains / Core Memories

**Technology:** VLSI